Dept. Electrical Engin. & Computing Systems UNIVERSITY OF Cincinna

5.3 Forward- and Reverse-Biased PN Junctions

Image is obvious for this lecture ... basic definition of what this component does?



This here is also a forward biased diode!



43 Gbit/s DPSK Balanced Photoreceiver

by Sam Hickman on November 17, 2010





This balanced photoreceiver from u²t Photonics is a differential front-end for 43 Gbit/s DPSK applications and features high differential gain of typically 2800 V/W.

The photoreceiver contains two waveguide-integrated pin-photodiodes on a single chip and a limiting amplifier

within one small form factor SMD-package. The limiting amplifier provides a differential output voltage swing of typically 600 mV.

SECS 2077 – Semiconductor Devices ©





> Zero bias, but we still have currents (but no net current).

$$J_{n}(drift) + J_{n}(diff.) = 0$$
$$J_{p}(drift) + J_{p}(diff.) = 0$$
$$(\mu_{p}p(x) E(x) = qD_{p}\frac{dp(x)}{dx}$$

▶ DRIFT: <u>Minority</u> carrier generation near (<L_n or <L_p) near junction, swept across (E).

► DIFF: <u>Majority</u> carriers with enough energy to climb barrier (at high E end of dist. tail)





■ Forward Bias...

Dept. Electrical Engin. & Computing Systems

- Forward bias:
 - voltage moves bands!
 - smaller deplet. reg.
 - smaller "V_o" (why? see gray)
 - E_C , E_V , E_F

How will my drift or diffusion currents change?

 DRIFT current: <u>NO CHANGE!!!</u>
 limited by minority carrier generation rate (not V):

 $n_p \text{ or } p_n$

- DIFF current: <u>CHANGES</u>
 - same majority carrier conc.
 - same energy distribution
 - but lower barrier to climb!

 $J \sim J(diff.)$

Net current flow, increases with V_f!

SECS 2077 – Semiconductor Devices ©



- Reverse Bias...
- Reverse bias:
 - wider deplet. reg.
 - larger "V_o" (again, why?)
 - E_C , E_V , E_F

How will my drift or diffusion currents change?

- DRIFT current: NO CHANGE
 - sure, E-field increases, but...
 - limited by minority carrier generation rate (no V)
- DIFF current: <u>CHANGES</u>
 - much larger barrier to climb!
 - quickly becomes zero

$$J \sim J(drift)$$

Could we use as a temperature sensor? an optical sensor?

SECS 2077 – Semiconductor Devices ©





Dept. Electrical Engin. & Computing Systems









Forward bias.
 J(diff.) >> J(drift.)
 J ≈ J(diff.)

More voltage, smaller barrier, increase in majority carrier diffusion, increase in current.



 $V = -V_r$



• Reverse bias. $J(diff.) \rightarrow 0$ $J \approx J(drift.)$

More voltage, larger barrier, only remaining current is minority carrier gen. near the junction.





Review!

8

Dept. Electrical Engin. & Computing Systems



Positive voltage moves the bands in what direction?

• Which side gets positive voltage to <u>forward bias</u> the diode?

- Why is the current exponential in <u>forward bias</u>? S7
- ► Why is the current in <u>reverse bias</u> constant with voltage? <u>S4</u>





A lot of Information

9

UNIVERSITY OF

- A lot of information...
- The diode equation is critically important.
- <u>Any</u> confusion/questions? Now is the time!
- Next, let's develop a way to predict reverse saturation current... if we have that we have most of what we need!

$$I = I_0(e^{qV/kT} - 1)$$

FYI, what we have done so far was 'easy', the next topics are going to get a bit more complicated. (hang on!)

This next section will also be very important for BJTs!



10 ■ Closer Look at Carrier Injection

We use Na/Nd since 2-sides to diode:

$$p_p = N_A \qquad n_n = N_D, \quad p_n = \frac{n_i^2}{N_D}$$
$$V_0 = \frac{kT}{q} \ln \frac{N_A N_D}{n_i^2}$$

$$\frac{p_p}{p_n} = \frac{n_n}{n_p} = e^{qV_0/kT}$$

- Under forward bias, J(diff) increases.
- Under reverse bias, J(diff) decreases.

► J(diff) involves injecting majority carriers across the junction where they then become minority carriers

► Therefore the minority carrier concentration is changed <u>near the</u> <u>junction:</u> $p(-x_{p0}) \approx p_p$

$$\frac{p(-x_{p0})}{p(x_{n0})} = e^{q(V_0 - V)/kT} = e^{qV_0/kT} e^{-qV/kT} = \frac{p_p}{p_n} e^{-qV/kT}$$

basically, this equation says as V approaches V_0 , hole conc. gets closer to equal at both depl. edges., makes sense... right?

SECS 2077 – Semiconductor Devices ©



 $\frac{p(x_{n0})}{p_n} = e^{qV/kT}$





Cincinnc





Interesting, what does this eq. look like?



Dept. Electrical Engin. 8

Forward bias - increased minority carriers <u>near</u> the junction:
Reverse bias - NO minority carriers <u>near</u> the junction:

Higher or lower compared to what? What is 'near'? $\Delta p_n = -p_n \quad (p = 0!)$ What will the minority carrier concentration look like in this 'near' region?

 Recall from Lecture 2... if we have an abrupt (delta function, like edge of depletion region) excess of holes (Δp) at a point x=0...

$$L_p = \sqrt{D_p \tau_p}$$

~ 1's to 100 \mum D_p = $\frac{kT}{q} \mu_p$ $\tau_p = \frac{1}{\alpha_r (n_0 + 1)^2}$

SECS 2077 – Semiconductor Devices ©

$$p(x) = p_n + \delta p(x)$$

$$\Delta p \int_{p_n} \delta p(x) = \Delta p \times e^{-x/L_p}$$

$$p_n \int_{0} \delta p(x) = \Delta p \times e^{-x/L_p}$$
Instructor – Prof. Jason Heikenfeld

UNIVERSITY OF

 $\Delta p_n = p_n(e^{qV/kT})$

12 ■ Concentration Profile (Forward)

Introduce new axis, variables



Why is p_n higher than n_p ?

Note, difference should be greater, diagram is qualitative (diagram at right is not log scale because carrier conc. lines are not straight).

SECS 2077 – Semiconductor Devices ©

Dept. Electrical Engin. & Computing Systems

Cincinna

Forward bias, diff dominates...





SECS 2077 – Semiconductor Devices ©



■ 15 ■ Junction Current

UNIVERSITY OF

Cincinnati

Therefore in forward bias, our current components are (diff.):



16 ■ Junction Current

Dept. Electrical Engin. & Computing Systems UNIVERSITY OF

• Therefore in forward bias, our current components are (diff.):



SECS 2077 – Semiconductor Devices ©

Understand

FOR p+n:

Why?

Standing Rev. Sat. (I₀)

$$I = qA\left(\frac{D_p}{L_p}p_n + \frac{P_n}{L_n}n_p\right) \times (e^{qV/kT} - \frac{P_n}{I} + \frac{P_n}{I}) = I_0(e^{qV/kT} - 1)$$

1)

REVERSE BIAS:

 $P_{p} \uparrow n_{p} \Psi I_{0} \Psi$ (less e⁻ avail. on one side for I_{driff}) and I_0 dominated by hole drift from n-side (p_n)

FORWARD BIAS:

Doping increases... larger V_0 ! Less currrent (I_0).

 $V_0 = \frac{kT}{q} \ln \frac{N_a N_d}{n_i^2} \qquad V \text{ (In) feeds into Jdiff (exp,)}$ Fermi) ... so net effect is almost linear with doping! $(p_n = n_i^2/N_D)$

GENERAL

Doping levels \uparrow I₀ \checkmark Doping levels Ψ I₀ \uparrow

Higher doping, less current!? YES!

$$L_p = \sqrt{D_p \tau_p} \quad \frac{D_n}{\mu_n} = \frac{kT}{q} \quad \tau_n = \frac{1}{\alpha_r (n_0 + p_0)}$$

SECS 2077 – Semiconductor Devices ©



18 \blacksquare Understanding Rev. Sat. (I₀)

► Lets use our new results to calculate the current <u>components</u> using another method... This second method will be important to understanding the next slide...



19 ■ Understanding Rev. Sat. (I₀)

I must be constant:

 $I = I_p + I_n$

Remember: $I_p \& I_n$ were calculated <u>independently</u>, hopefully they add up!

Under forward bias:(1) Diffusion(2) Recombination

Note offset and what dominates for p+n diode:

p+ side - hole diffusion

n side - electrons brought in mainly *just to support hole recombination*





Key points for forward bias I : -heavy doped side dominates (but higher doping decreases total I !) -further away junction (drift, small E)

■ 20 ■ Review!

Dept. Electrical Engin. & Computing Systems

p-type





UNIVERSITY OF

0V

► How does doping effect <u>forward bias</u> current, and <u>why is it this way</u>? ... <u>S17</u>

How does doping effect <u>reverse bias</u> current, and why? <u>\$17</u>

► What do the minority carrier profiles for <u>forward bias</u> look like near the depletion region edge? <u>S12</u>

► What do the minority carrier profiles for <u>reverse bias</u> look like near the depletion region edge? S13

▶ In the formula shown at right, where do I input my parameters from and why?



SECS 2077 – Semiconductor Devices ©